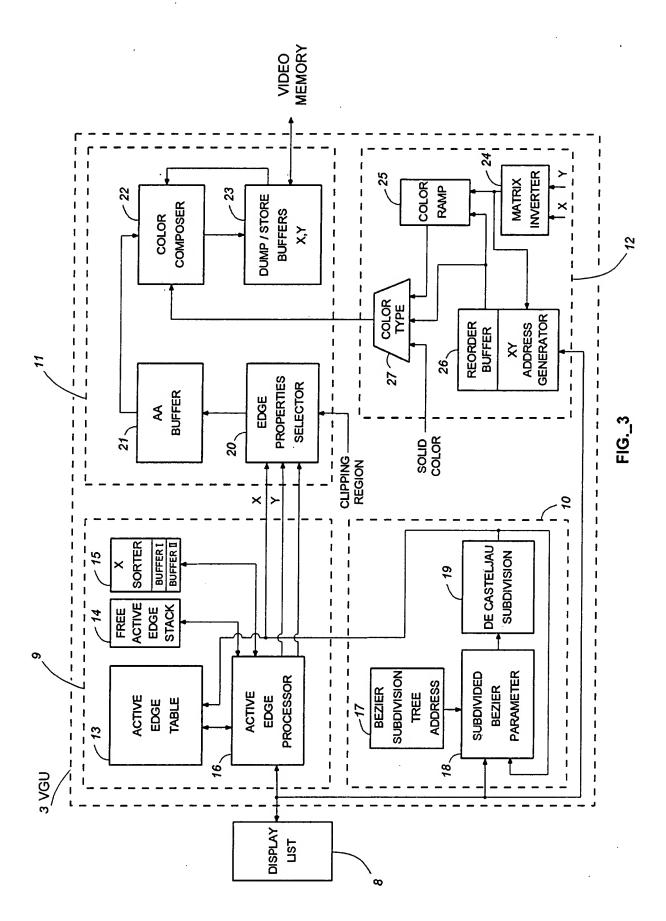


FIG._2



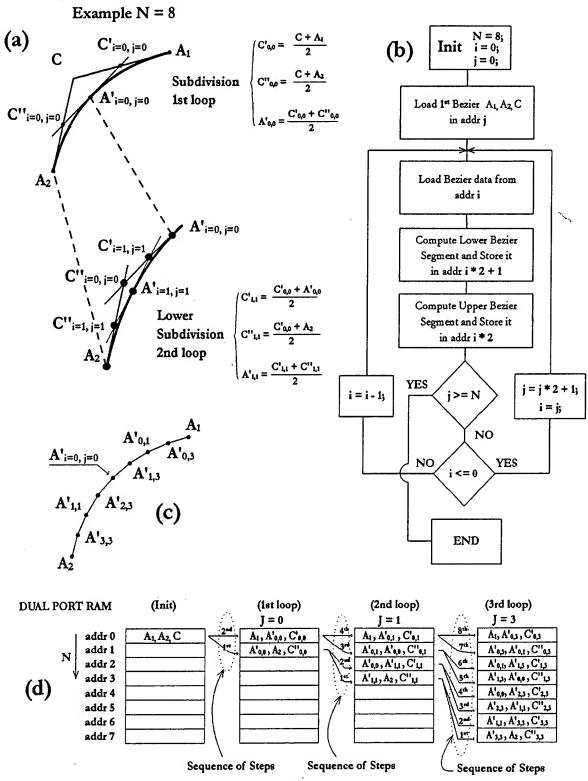


FIG. 4

Free Active Edge Stack (LIFO Stack Memory)

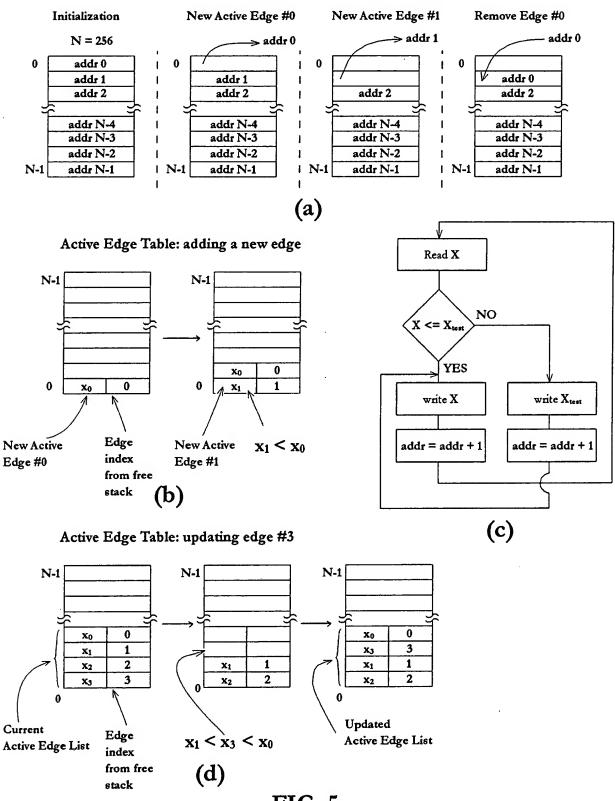
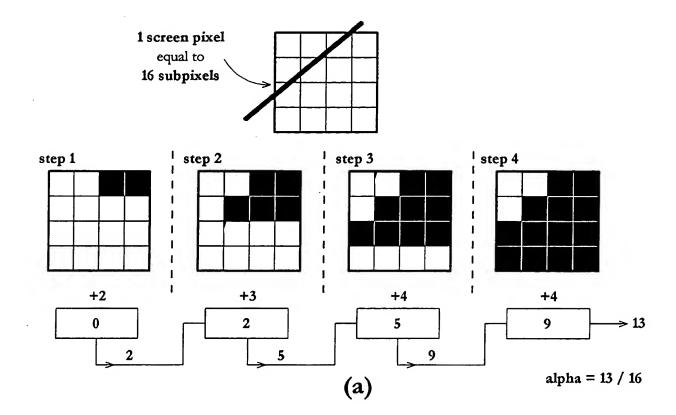
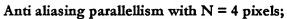
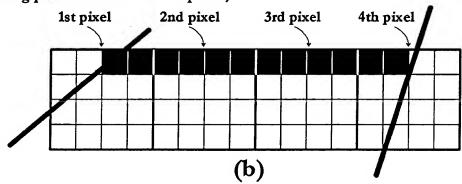


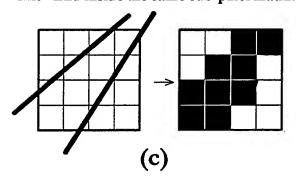
FIG. 5







Init - End inside the same sub-pixel matrix



Dual Port

Memory

Pixel 0 + j * 4

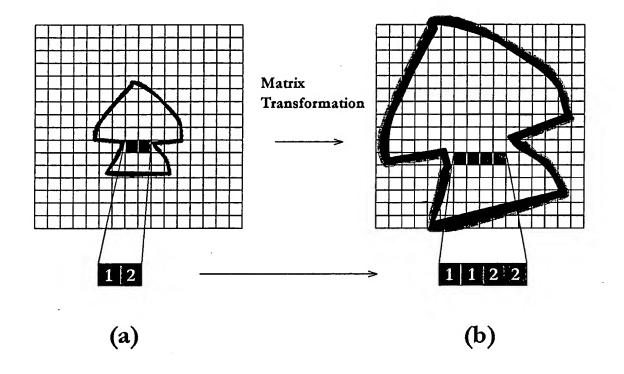
Pixel 1 + j * 4

Pixel 2 + j * 4

Pixel 3 + j * 4

(d)

FIG. 6



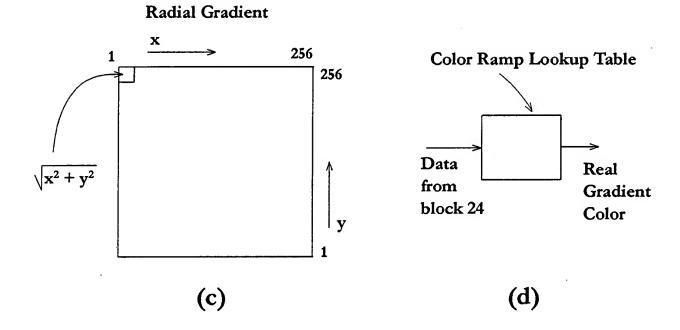
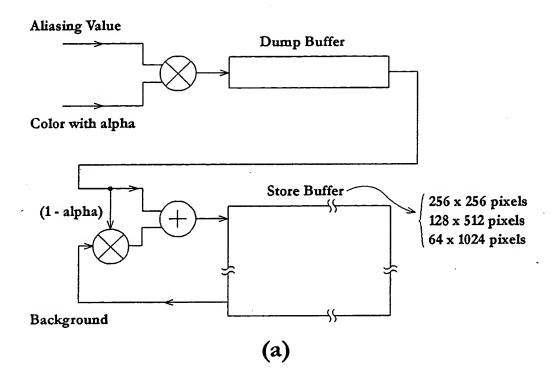


FIG. 7



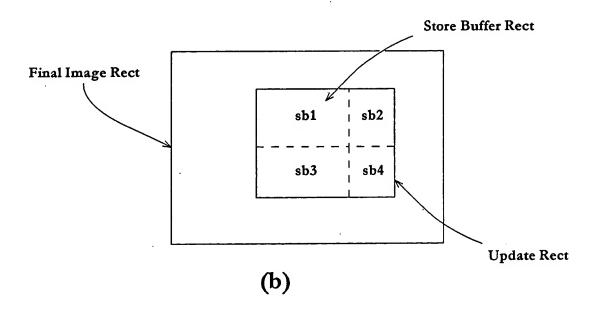


FIG. 8